

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

En-Hsing Chen et al.

Title:

NAND MEMORY ARRAY INCORPORATING MULTIPLE SERIES

SELECTION DEVICES AND METHOD FOR OPERATION OF SAME

Application No.: 10/729,865

Filed:

December 5, 2003

Examiner:

Unknown

Group Art Unit:

2818

Atty. Docket No.: 023-0029

Confirmation No.:

8494

May 27, 2005

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## INFORMATION DISCLOSURE STATEMENT 37 C.F.R. § 1.97(b)

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	Form(s) PTO/SB/08B (N/A page(s), with copy of no reference(s)).
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This Information Disclosure Statement is filed within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d) or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.

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Andrew C. Graham Date

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EXPRESS MAIL LABEL:

PTO/SB/08A (Substitute for form 1449/PTO)	Attorney Docket No.: 023-0029		
	Application No.:	10/729,865	
INFORMATION DISCLOSURE TATEMENT BY APPLICANT	First Named Inventor: Roy E. Scheuerlein		
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Sheet 1 of 1	Date Submitted:	May 27, 2005	

			U.S. PATENT DO	CUMENTS		
Examiner Initials*	Cite No.1	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, W Relevant Passages or Rele Figures Appear	here vant
		6,380,636	04-30-2002	Tatsukawa et al.		
		US-2004/0145024 A1	07-29-2004	Chen et al.		
			FOREIGN PATENT	DOCUMENTS		
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> -Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
Examiner Sig	gnature			Date Considered		<u></u>

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